

J1036 U.S. PTO

卷之三

**SUITE CLASSIFICATION**

PATENT NUMBER

**U.S. UTILITY Patent Application**

O.I.P.E.

**PATENT DATE**

SCANNED BY J.A.

[View all posts by \*\*John\*\*](#) [View all posts in \*\*Uncategorized\*\*](#)

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART.UNIT	EXAMINER
09/836375	D	713	500	2182	S. Kawachi Lee

## APPlicants

Peter Cook  
Stanley Schuster

三

## Latch structure for interlocked pipelined CMOS (IPCMOS) circuits

PTO-2040  
12/89

**BEST AVAILABLE COPY**

## **ISSUING CLASSIFICATION**

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
<b>INTERNATIONAL CLASSIFICATION</b>							

**Continued on Issue Slip Inside File Jacket**

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____		(Assistant Examiner)	(Date)		
				<b>ISSUE FEE</b>	
		(Primary Examiner)	(Date)	Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of				<b>ISSUE BATCH NUMBER</b>	

**WARNING:**

The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.

Form PTO-436A  
(Rev. 8/99)

FILED WITH:  DISK (CRF)  FICHE  CD-ROM

(Attached in pocket on right inside flap)

(FACE)